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02/20/02
Jcw # 912

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10079447	02/20/2002	327	4	2816	LEE

**APPLICANTS: Gabara Thaddeus; Martin Samuel;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/270,263 02/21/2001

** FOREIGN APPLICATIONS VERIFIED

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 290-051us
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiners's initials		

TITLE : Method for modeling noise emitted by digital circuits

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED				
ISSUE FEE Amount Due Date Paid		Assistant Examiner		Total Claims		
		Primary Examiner		Print Claim for O.G.		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		DRAWING		
		Application Examiner		Sheets Drwg.	Figs. Drwg.	Print Fig.
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